



501.38642X00

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e.e.  
09/11/03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: T. YAMADA, et al.

Application No.: 09/603,965

Filed: June 26, 2000

For: LOW POWER CONSUMPTION MICROPROCESSOR

Art Unit: 2181

Examiner: K. S. Kim

**RECEIVED**

AUG 29 2003

Technology Center 2100

**RESPONSE TO ELECTION/RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 28, 2003

Sir:

In response to the Restriction Requirement dated April 9, 2003, Applicants hereby elect the **Group I** claims 1-10, 32-28, 52 and 56 drawn to a microprocessor supplying a substitute instruction for predetermined instructions, classified in class 712, subclass 226, without traverse.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 501.38642X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By 

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GEM/dlt

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